

The diagram illustrates the architecture of a smart card system, showing the interaction between several components:

- 101: MODULE DE RÉCUPÉRATION DES TABLES DE SIGNALISATION** (Signal Table Recovery Module): Receives **PID** (A1) from the card and sends **STORING PERSONAL IDENTIFICATION DEVICE STOCKAGE PID** (A1bis) to the card.
- 112: SMART CARD READER LECTEUR DE CARTE À PUCE** (Smart Card Reader): Interacts with the card via **CARTE INSÉRÉE** (A2), **LIRE ADRESSE** (A3), **AD** (A4), **EMM** (A5), **CARTE EXTRAITE** (A6), and **EMM RECUE** (A7).
- CA: CARD**: The central component, which also receives **POSE CONFIG** (A5) and **ERASE CONFIGURATION EFFACE CONFIG** (C2) from the reader.
- 102: MODULE DE GESTION DES BUFFERS** (Buffer Management Module): Receives **EMM** (A6) from the card and sends **ERASED CONFIGURATION CONFIGURATION EFFACEE** (D1) and **CONFIGURATION** (E1) to the Rights Storage Module.
- MD: RIGHTS STORAGE MODULE**: Receives **EMM** (E2) from the Buffer Management Module and sends **POSE CONFIG** (B2) and **EMM** (D3) back to the card. It also outputs **B1bis** and **D3bis** to the **STOCKAGE CONFIG.** and **STOCKAGE EMM** modules respectively.
- Filters (FILTRES)**: A block on the right that receives signals from the card and the Rights Storage Module.

**Legend:**

- A7...EMM RECEIVED
- A8...READ EMM
- A5...READ ADDRESS
- C1...EXTRACTED CARD
- EMM...ENTITLEMENT MANAGEMENT MESSAGE